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Application Number	NEW APP
Filing Date	
First Named Inventor	Carballo
Art Unit	
Examiner Name	
Attorney Docket Number	AUS9 2003 0656 US1

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
APL		WEI-HUNG CHEN; DEHNG, G.; JONG-WOEI CHEN; SHEN-LUAN LIU; "A CMOS 400-Mb/s serial link for AS-memory systems using a PWM scheme", IEEE Journal of Solid-State Circuits, Volume: 35 Issue: 10, Oct 2001, Pages 1498-1505	
APL		KEN YANG CHIH-KONG; RAMIN FARJAD-RAD; M.A. HOROWITZ; "A 0.5-/spl mu/m CMOS 4.0-Bgit/s serial link transceiver with data recovery using oversampling", IEEE Journal of Solid-State Circuits, Volume: 3, Issue: 5, May 1998, Pages 713-722	
APL		KHOURY, J.M.; LAKSHMIKUMAR, K.R.; "High speed serial transceivers for data communication systems", IEEE Communications Magazine, Volume: 39, Issue: 7, July 2001, Pages: 160-165	
APL		J. KIM and M. HOROWITZ; "Adaptive Supply Serial Links with Sub-1V Operation and Per-Pin Clock Recovery", ISSCC 2002.	

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